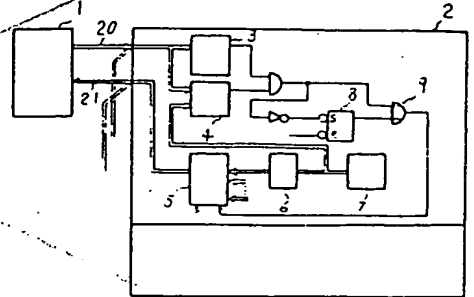


(54) MULTI-SELECTION DETECTING CIRCUIT

(11) 1-309156 (A) (43) 13.12.1989 (19) JP
 (21) Appl. No. 63-139289 (22) 8.6.1988
 (71) HITACHI LTD (72) MASATOSHI NISHINA(1)
 (51) Int. Cl. G06F13/00

PURPOSE: To prevent the multi-selection of drivers by making the drivers set in the addresses except the one designated by a selection command give answers to this command.

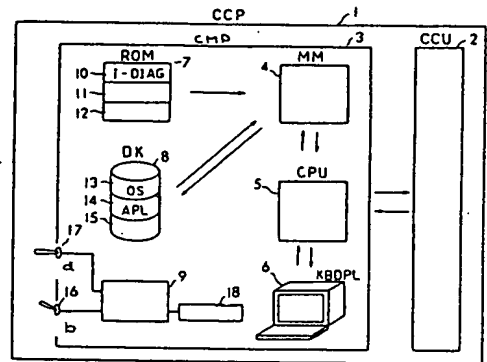
CONSTITUTION: When a selection command is given to a driver 2 from a controller 1, the selection command is decoded by a command decoding circuit 3 and at the same time an address comparator 4 compares the address of the driver 2 with the address transferred via a control circuit. For a selected driver, the address set via an address setting part 7 is coded by an address coding circuit 6 and sent to the controller 1 via a selection circuit 5 and a bus line 21. This address code is also sent to the line 21 via an OR circuit 9 even with a driver already kept under a selection state. As a result, the controller 1 detects the abnormality of the address code even in case 2 drivers are selected at one time or separately with a time interval. Thus a multi-selection state can always be detected.

**(54) CMP CONTROL SYSTEM**

(11) 1-309157 (A) (43) 13.12.1989 (19) JP
 (21) Appl. No. 63-140302 (22) 7.6.1988
 (71) FUJITSU LTD (72) FUMIO HOSHI(4)
 (51) Int. Cl. G06F13/00, H04L13/00

PURPOSE: To obtain a means which can check the fault of a CMP before the rise of an OS by performing a retrying job in a user mode and delivering the control of a mini-OS in a maintenance mode in case an error occurs during the execution of an initial diagnosing.

CONSTITUTION: A mini-OS 12 is stored in a nonvolatile memory 7 together with an initial diagnosing program 10 and a loader 11 and then prepared in a main memory 4 and held there until the execution of an OS 13 is started. Then the mini-OS 12 is started in a maintenance mode according to the state of a changeover switch 16 in case the fault of the CMP. Thus a fault checking means is offered to a maintenance operator. While a retrying job can be continued as conventional in a user mode.



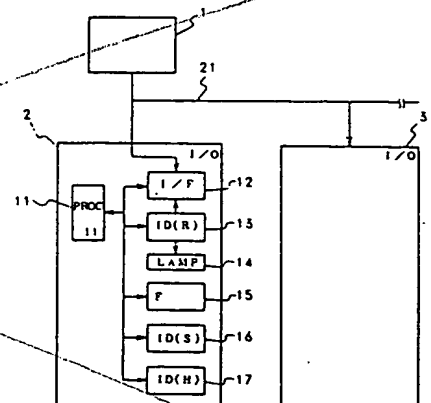
9: control circuit, 18: mode register, a: interruption switch, b: changeover switch

(54) DATA PROCESSING SYSTEM

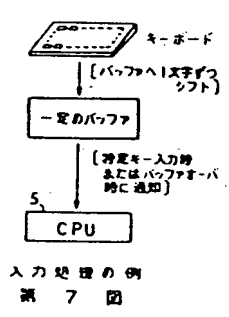
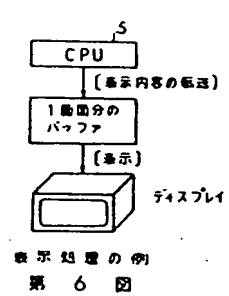
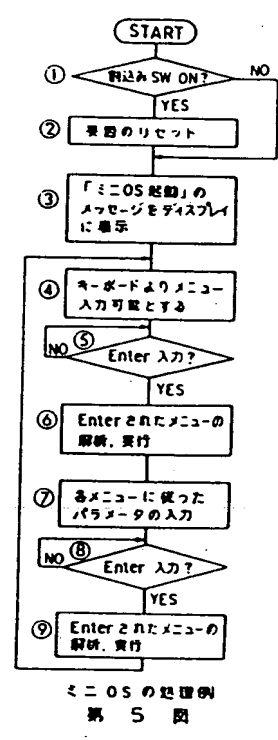
(11) 1-309158 (A) (43) 13.12.1989 (19) JP
 (21) Appl. No. 63-141259 (22) 7.6.1988
 (71) HITACHI LTD(1) (72) TETSUO FUJIWARA(1)
 (51) Int. Cl. G06F13/14

PURPOSE: To facilitate the setting change of the slave device numbers by using a physical device number setting means and a device number memory means whose set value can be changed under the control of a master device and deciding the preference of the device number set by the device number setting or memory means under the control of the master device.

CONSTITUTION: A device number memory means 13 whose set value can be changed under the command control of a master device 1 is prepared together with a physical device number setting means 17 like a jumper switch, a dip switch, etc. Then it is decided whether the preference should be given to the device number set at the means 13 or 17 under the control of the device 1. Thus the setting change is extremely simplified for the device numbers. Furthermore a fixed device number is always set and selected by means of a nonvolatile selection memory means 15 and the means 13 with no use of any physical means. As a result, the slave device numbers can be continuously reset despite the break of a power supply.



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第1頁の続き
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